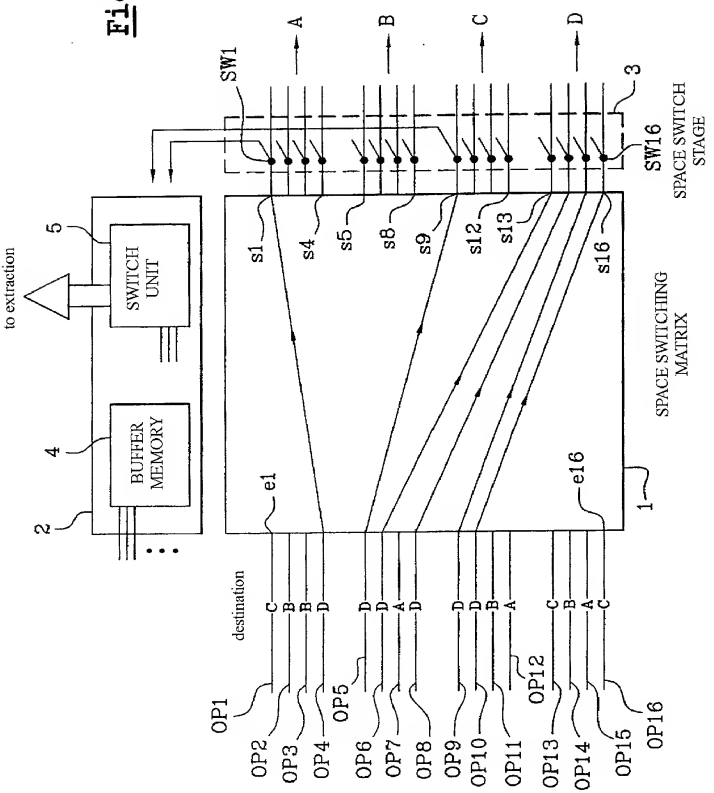
1/1

Fig. 1


to extraction
5
SWITCH UNIT
4
BUFFER MEMORY
2
SW1
A
B
C
D
3
SW16
SPACE SWITCH STAGE
s1
s4
s5
s8
s9
s12
s13
s16
e1
e16
1
SPACE SWITCHING MATRIX
destination
OP1
OP2
OP3
OP4
OP5
OP6
OP7
OP8
OP9
OP10
OP11
OP12
OP13
OP14
OP15
OP16